# Advertisement for SRF Position

Applications are invited from motivated and eligible candidates for the position of Senior Research Fellow (SRF) in a DST funded Research Project titled " **Reducing computational complexity using FPGA to study structure and dynamical properties of nanoparticles**".

**Objectives of the Project**
The objective of the project is to reduce computing time of a computationally intensive code by running it on a massively parallel FPGA platform.  To gain understanding of the parallel structure in FPGA, a simple mathematical operation (matrix multiplication) involving huge data needs to be implemented. This will be followed by development of artificial neural network based interatomic potential for gold nanoparticles on FPGA platform. Interested candidates can email for more information.

**Essential Qualification:**
(i) M.Tech./M.E. degree and 2 years of research experience in Computer Science and Engineering/Electrical and Electronics Engineering/or in equivalent streams with a valid GATE score.

OR
(ii)  M.Sc degree in Chemistry/Physics and 2 years of research experience with a valid GATE  or CSIR/UGC-NET or JEST score.

OR
(iii) Candidates not having the required qualification but feel thay are suitable for this project, may also apply by providing justification and qualifications. These candidates may be considered for a JRF position.

**Skillsets:**
Candidates must have experience in digital electronics (basics in microprocessors / micro-controllers). Exposure to programming languages such a C, C++, embedded C and hardware description languages such as VHDL, Verilog etc. are desirable.

**Stipend**
The amount of SRF fellowship will be as per DST norms.

**Duration**
The project duration is for 3 years and the initial appointment will be for one year,
and is extendable further based on satisfactory performance.
**How to Apply**
1. Interested candidates are requested to submit a detailed CV to Dr. Satya S. Bulusu, Discipline of Chemistry, Indian Institute of Technology Indore, via email:sbulusu@iiti.ac.in latest by August 20, 2017.
2. Complete information with respect to GATE, NET or JEST score such as year of passing, discipline, score, marks, All India Rank and number of candidates appeared should be mentioned in the CV. Incomplete applications will be rejected. The deadline for submission of applications is **Aug 31, 2017**.
3. Only shortlisted candidates will be be intimated by email for interview. Selected candidates will be intimated by e-mail. No TA/DA will be paid for appearing in the interview. No communication with regard to to selection process would be entertained.
4. Merely fulfilling the eligibility will not entitle any candidate for shortlisting