



Indian Institute of Technology Indore

IIT
Indore

IET Campus: IET-DAVV Campus, Khandwa Road, Indore – 452 017 (M.P.)

PACL Campus: 113/2-B (Opp. to Veterinary College), Village Hernia Khedi, MHOW Road, Indore 453 446 (M.P.)

Dated: May 7, 2015

Advertisement for JRF Position

Applications are invited from motivated and eligible candidates for the position of Junior Research Fellow (JRF) in a DST funded Research Project titled "**Quasi Z-source based standalone PV system**". The aim of this project is to develop a standalone PV system with quasi Z-source inverter or its variations. Applicants should have sound basics in power electronics and also should have knowledge of hardware/pcb design and microcontroller/microprocessor programming.

Eligibility:

- (i) M.Tech./M.E. degree in relevant branch of Electrical Engineering, OR
- (ii) B.Tech./B.E. degree in Electrical Engineering/Technology with *valid* GATE score

Stipend: The amount of JRF fellowship will be as per DST norms. Refer to funding agency website for more details.

Duration: Initial appointment is for one year, which is extendable up to 3 years solely based on performance.

Interested candidates are requested to submit a detailed CV to **Dr. Amod C. Umarikar**, Discipline of Electrical Engineering, Indian Institute of Technology Indore, via e-mail: umarikar@iiti.ac.in. Please note that your CV should include contact details (address, phone no., email ID), date of birth, qualifications clearly mentioned. Complete information with regards to GATE score such as: year of passing and validity, discipline, score, marks, all India Rank and number of candidates appeared should be mentioned in the CV. Incomplete applications will be rejected.

Last Date to Receive Applications: May 22, 2015

Only shortlisted candidates will be called for interview. Selected candidates will be intimated by email. No TA/DA will be paid for appearing in the interview.