



भारतीय प्रौद्योगिकी संस्थान इन्दौर
अभियांत्रिकी एवं प्रौद्योगिकी संस्थान, डी ए टी वी परिसर, खंडवा रोड,
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IIT Indore

Indian Institute of Technology Indore

Institute of Engineering and Technology, DAVV Campus, Khandwa Road,
Indore 452 017, Madhya Pradesh, India

Dated: May 02, 2015

Ref.: IITI/EE /AK/DST/2015/02

Advertisement for Junior Research Fellow

Intelligent, motivated and eligible candidates are encouraged to appear for a **walk-in interview** to work as a Junior Research Fellow (JRF) in a DST funded Research Project entitled "Evaluation of Junctionless Transistor Architecture for Next Generation CMOS Devices and Circuits". The aim of the project is to analyze the advantages and challenges of nanoscale Junctionless MOSFETs and establish an early understanding of their potential impact on device and circuit design of next generation MOS devices. For more information, please visit the following web link: <http://www.abhinavkranti.yolasite.com>

Stipend: Rs. 16,000 / Rs. 18,000 + 20% HRA/month (sanctioned by the funding agency)
Duration: Till the completion of the project

Eligibility:

- M.Tech./M.E. degree in relevant branch of Electrical/Electronics Engineering, OR
- B.Tech./B.E. degree in Engineering/Technology or equivalent degree (M.Sc. in Physics/Electronics) with valid GATE score OR valid UGC-CSIR NET Junior Research Fellowship (JRF) OR any equivalent fellowship
- Candidates should have a minimum of 60 % (or equivalent) in graduation/post-graduation

Age limit: 28 years. Relaxation for SC/ST/OBC candidate as per Government of India norms.

Candidates fulfilling the above eligibility qualifications are strongly encouraged to appear for a walk-in interview, the details of which are given below. The candidates should bring the following documents at the time of interview:

- Covering letter and detailed Curriculum Vitae (CV) along with valid photographic identity card and passport size photograph.
- Original and attested photocopies of all documents, certificates, degrees and mark sheets (Xth class onwards). Original and one attested photocopy of GATE score card and/or UGC-CSIR JRF/NET examination result, if applicable.
- Copies of certificates/testimonials/documents which the candidate may wish to bring to the knowledge of the selection committee.

Time and date of walk-in interview: 09:00 hrs on May 09, 2015 (Saturday).

Venue: IIT Indore, PACL Campus, Survey No. 113/2-B (Opposite Veterinary College), Mhow Road, Village Harnia Khedi, Tehsil Mhow, District-Indore, Pin code 453441, Madhya Pradesh.

Candidates claiming age relaxation should clearly state the same in the covering letter. Complete information in terms of GATE and/or JRF (NET) such as year of passing and validity, discipline, score, marks, All India Rank and number of candidates appeared should be mentioned in the CV. Candidates may also email a copy of their detailed CV to Dr. A. Kranti at akranti@iiti.ac.in. Information on ways to reach PACL campus is available at:

<http://www.iiti.ac.in/Institute/How to find us/by bus location.htm>

The JRF appointment will be on a purely temporary basis and co-terminus with the project. No TA and/or DA will be paid for attending the walk-in interview and/or written test. Candidates should make their own arrangements for stay.