



Indian Institute of Technology Indore

Khandwa Road, Simrol, 453552.

Office Ph: +917316603516

IIT

भारतीय प्रौद्योगिकी संस्थान इन्दौर
सिमरोल, इन्दौर 453 552, भारत

Advertisement for Recruitment of Research Engineer

Advt. No: ACPSLab/DISHA1

Applications are invited from interested and motivated candidates for the post of Research Engineer in a research project as per the following details.	
Position	Research Engineer(RE)
Number of Vacancy	01
Project Title	“Forward Compatible OpenOneIoT–HarSaRK SaMD Framework for Low-Cost Digital Health Products”
Department	Computer Science and Engineering
Fellowship duration	12 months.
Job Description	<p>The selected candidate will work on a recently awarded DISHA 2.0 project focused on developing advanced system frameworks for high-reliability intelligent applications. Responsibilities include:</p> <ul style="list-style-type: none">• Development of distributed sensing and actuation modules with strict performance requirements on microcontroller boards• Implementation of software and Android SDK-based mobile applications for real-time data processing• Integration of system components into a unified platform architecture• Design and execution of testing and validation frameworks• Support for compliance-oriented development practices• Willingness to learn and apply guidelines for high-quality SaMD systems• Participation in pilot deployments and field validation• Contribution to secure and scalable system operation• Documentation and coordination with project stakeholders

Essential Qualification	<p>1. BTech/BEgg/MTech/MEgg degree in Computer Science and Engineering, Information Technology, Electronics Engineering or relevant branch of Engineering or Technology.</p> <p>2. Preference will be given to candidates with:</p> <ol style="list-style-type: none"> At least one year of systems and/or software development/research experience. Hands-on programming expertise. Experience of implementing systems from ground up.
Age Limit	25 years
Fellowship	<p>1. For BTech/BEgg holders with 1-2 years experience: Rs. 37,000/- p.m. plus HRA will be provided as per institute norms.</p> <p>2. For BTech/BEgg holders with more than 2 years experience: Rs. 42000/- p.m. plus HRA will be provided as per institute norms.</p> <p>3. For MTech/MEgg holders who have either :</p> <ol style="list-style-type: none"> done a systems project in their dissertation on an end-to-end basis or have at least one year of job experience in systems design and development: <ul style="list-style-type: none"> --- Rs. 42,000/- p.m. plus HRA will be provided as per institute norms.

Interested candidates are requested to submit a detailed softcopy of their CV with all relevant Essential Qualification Degree Certificates, Marks Sheets, GATE qualifying certificates, Date of Birth Pro Certificate in a single pdf file to gourinath@iiti.ac.in by 15/05/2026. Tentative Date of Interview: Last **week of May 2026**.

Only shortlisted candidates will be called for interview. Selected candidates will be intimated by email.

No TA/DA will be paid for appearing in the interview. Address for Correspondence:

Prof. Dr GOURINATH BANDA (email: GOURINATH@iiti.ac.in)

Department of Computer Science and Engineering

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