



Indian Institute of Technology Indore

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भारतीय प्रौद्योगिकी संस्थान इंदौर

निम्नानुसार, खंडवा रोड,
निम्नानुसार
इंदौर - 453552 (म.प्र.), भारत

IIT Indore

Dated: August 07, 2023

Advertisement for the post of Junior Research Fellow (02) and Senior Research Fellow (01)

Applications are invited from highly motivated candidates for the positions of **Junior Research Fellows (JRFs) and Senior Research Fellow (SRF)** in sponsored project of **Ministry of Electronics & Information Technology (MeitY)** under **Chip to Startup (C2S)** initiative for the project entitled **"Implantable Pacemaker Chip (iPACE-CHIP)"** at Department of Electrical Engineering, Indian Institute of Technology Indore.

Aim of Project: The aim of the project is to design and fabricate the next generation chip (iPACE-CHIP) for implantable pacemaker to be used in the commercial product of Shree Pacetronix Ltd. with enhanced features. For more information about the project and our research group please click at www.skvishvakarma.com

Eligibility for JRF:

M.E./M.Tech./MS(R) in Microelectronics and VLSI Design / VLSI Design/ VLSI Design and Nanoelectronics/ Embedded System / Electronics / Electronics & Communication/ Electronics and Electrical/ Electronics and Instrumentation / Digital Systems / Digital Instrumentation/ Computer Science and Engineering and other related areas of Electronics Engg with a valid GATE score.

OR

Bachelor/M.Sc. degree in Electronics / Electronics & Communication/ Electronics and Electrical/ Electronics and Instrumentation / Electrical / Instrumentation / Computer Science and Engineering/ equivalent branches with a valid GATE score or a valid UGC/CSIR-NET qualification.

Eligibility for SRF:

M.E./M.Tech./MS(R) in Microelectronics and VLSI Design / VLSI Design/ VLSI Design and Nanoelectronics/ Embedded System / Electronics / Electronics & Communication/ Electronics and Electrical/ Electronics and Instrumentation / Digital Systems / Digital Instrumentation/ Computer Science and Engineering and other related areas of Electronics Engg with a valid GATE score and **two years of experience** in teaching/research/industry in the field of Microelectronics and VLSI Design/ Electronics Circuit and System Design or other relevant areas.

Desirable Qualification:s for above two posts: Experience of Design and Fabrication of Semiconductor Chip, experience to work on EDA Tools, understanding of Linux environment, Exposure to install the EDA

tools, Experience to handle the VLSI training for capacity building etc.

Age limit: As per DST and our institute norms.

Salary:

JRF: INR 31,000/- per month (Consolidated emoluments) + House Rent Allowance (as per DST norms).

SRF: INR 35,000/- per month (Consolidated emoluments) + House Rent Allowance (as per DST norms).

Duration:

Initial appointment is for one year, which is extendable up to duration of the project. Selected candidates will be encouraged to admit into the PhD Programme as a regular full-time research scholar at IIT Indore.

How to apply?

Interested applicants are requested to submit a detailed resume to Dr. Santosh Kumar Vishvakarma, Professor, Department of Electrical Engineering, Indian Institute of Technology Indore, via e-mail: skvishvakarma@iiti.ac.in with the subject line “**Application for JRF/SRF position for iPACE-CHIP**” by **August 18, 2023 by 5:00 PM**. The applications will be scrutinized and **only shortlisted candidates will be intimated through email for an interview**. The interview will be held online, tentatively in the month of August, 2023.