भारतीय प्रौद्योगिकी संस्थान इन्दौर

भिमरोल, डन्टीर ४५३ ५५२, भारत

Indian Institute of Technology Indore

Simrol, Indore 453 552, India

कार्यालय: +९१ ७३२४ ३०६७०९ +98 938 3836008 फैक्स : +९१ ७३१ २४३८७२१

Office: +91 7324 306709 +91 731 2438709 Fax: +91 731 2438721

Advertisement for Walk-in Interview for the post of Research Associate (RA) in DST funded project

Dated: February 03, 2020

No.: IITI/EE/AK/2020/01

Discipline of Electrical Engineering, Indian Institute of Technology Indore, invites applications from intelligent, motivated and eligible candidates to work as a Research Associate to fulfill the objectives of a DST funded research project entitled "Functionality Enhancement of Reconfigurable Transistors by Implementing Standalone and Embedded 1T-DRAM". The aim of the project is to analyze the advantages and challenges of reconfigurable transistors for standalone and embedded 1T-DRAM.

Fellowship: Rs. 47,000 + HRA (as per rules)

Duration of appointment: Initially for 12 months (co-terminus with the project)

Essential Qualification: PhD or having 3 years of research, teaching and design and development experience after ME/M. Tech. in relevant branch of Electronics / Electrical Engineering or equivalent with at least one research paper in Science Citation Indexed (SCI) journal. Candidate must have good knowledge of semiconductor device physics, operation of advanced MOS transistors, emerging transistor architectures for logic and memory, and prior experience of working on TCAD software. Good communication skills are essential for the sponsored research project.

Age limit: 35 years. Age relaxation for SC/ST/OBC/PwD candidates will be as per Government of India norms.

Interested candidates must email their complete Curriculum Vitae along with all relevant details including list of publications, degree certificates, mark sheets, and date of birth proof to Prof. Abhinav Kranti, Electrical Engineering, Indian Institute of Technology Indore, at akranti@iiti.ac.in. The subject of the email should be: CV for RA position

Time and date of selection process: 10 am on February 13, 2020. Interested candidates fulfilling the essential qualification outlined above should report for the selection process on February 13, 2020, at Room 314, Scandium Building, Indian Institute of Technology Indore, Khandwa Road, Simrol, Indore 453552, Madhya Pradesh. Candidates should prepare a 10 min presentation of the previous research work carried out by them. Applicants should ask at least one referee, who should have known the applicant in a professional capacity, to send a letter of recommendation at akranti@iiti.ac.in before February 13, 2019.

Important: The appointment is purely temporary and co-terminus with the project. No TA & DA will be paid for attending the interview. Mere fulfillment of the essential qualifications does not entitle the selection. The Institute reserves the right not to fill up the advertized post if the candidates are not found suitable for the post.