



## Indian Institute of Technology Indore

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IIT Indore

## भारतीय प्रौद्योगिकी संस्थान इंदौर

सिमरोल, खंडवा रोड,  
सिमरोल  
इंदौर - 453552 (म.प्र.), भारत

Dated: Oct 3, 2023

### **Advertisement for the post of Junior Research Fellow (02) and Senior Research Fellow (01)**

Applications are invited from highly motivated candidates for the positions of **Junior Research Fellows (JRFs)** and **Senior Research Fellow (SRF)** in sponsored project of **Ministry of Electronics & Information Technology (MeitY)** under **Chip to Startup (C2S)** initiative for the project entitled **"Implantable Pacemaker Chip (iPACE-CHIP)"** at Department of Electrical Engineering, Indian Institute of Technology Indore.

**Aim of Project:** The aim of the project is to design and fabricate the next generation chip (iPACE-CHIP) for implantable pacemaker to be used in the commercial product of Shree Pacetronix Ltd. with enhanced features. For more information about the project and our research group please click at <https://www.skvishvakarma.com/research/sponsored-research-projects>

#### **Eligibility for JRF:**

B.Tech./M.Sc./M.E./M.Tech./MS(R) in Electronics / Electronics & Communication/ Electronics and Electrical/ Electronics and Instrumentation / Computer Science and Engineering/ Microelectronics and VLSI Design / VLSI Design/ VLSI Design and Nanoelectronics/ Embedded System / Digital Systems / Digital Instrumentation and other related areas of Electronics Engg.

#### **Eligibility for SRF:**

B.Tech./M.Sc./M.E./M.Tech./MS(R) in Electronics / Electronics & Communication/ Electronics and Electrical/ Electronics and Instrumentation / Computer Science and Engineering/ Microelectronics and VLSI Design / VLSI Design/ VLSI Design and Nanoelectronics/ Embedded System / Digital Systems / Digital Instrumentation and other related areas of Electronics Engg and **two years of experience** in teaching/research/industry in the field of Microelectronics and VLSI Design or other relevant areas.

**Desirable Qualifications for above two posts:** Experience of Design and Fabrication of Semiconductor Chip, experience to work on EDA Tools, understanding of Linux environment, Exposure to install the EDA tools, Experience to handle the VLSI training for capacity building etc.

**Age limit:** As per DST and our institute norms.

**Salary:**

**JRF:** INR 31,000/- per month (Consolidated emoluments) + House Rent Allowance (as per DST norms).

**SRF:** INR 35,000/- per month (Consolidated emoluments) + House Rent Allowance (as per DST norms).

The salary will be revised as per directive of DST, Govt. of India.

**Duration:**

Initial appointment is for one year, which is extendable up to duration of the project. Selected candidates will be encouraged to be admitted into the PhD Programme, if they fulfill the IIT Indore's eligibility criteria in PhD admission.

**How to apply?**

Interested applicants are requested to fill this [Google Form](#) by **October 10, 2023** by 5:00 PM. The applications will be scrutinized and **only shortlisted candidates will be intimated through email for an interview**. The interview will be held online, tentatively in the month of October, 2023.