



Advertisement for the Position of JRF

Applications are invited from highly motivated and eligible candidates for the following project position at the Department of Electrical Engineering, IIT Indore.

Project Title: Design Prototype of Security Chip

Joint Research Fellow (JRF)

Number of Positions: 01

Stipend: Rs. 37,000/- per month consolidated

Duration: Initially for 6 months, extendable based on performance and project requirements.

Essential Educational Qualifications

- The minimum essential qualification is a M.Tech./M.S. (by research)/M.E. in Engineering
- Minimum 60% marks or equivalent CGPA
- Knowledge of SRAM architecture and hardware security is desirable

Key Responsibilities

- Design and simulation of secure SRAM architectures
- Development of prototype circuits
- Basic knowledge of RTL, Verilog, TCL scripting and Python.
- Documentation and reporting of project progress

Age Limit: 28

How to Apply

Interested candidates are requested to submit the following documents:

- a) A detailed CV
- b) One-page statement of purpose.
- c) All relevant Essential Qualification Degree Certificates, Marks Sheets, GATE/JEST/National Level Exam qualifying certificates, Date of Birth Proof Certificate All documents should be in a single PDF file

The above-mentioned details should be sent via email to: **nsdcs.ee@iiti.ac.in** with a subject line: **Application for Project JRF [Your Name]**.

The last date for submission of applications and references is **14th August 2026**.

For queries, contact: **nsdcs.ee@iiti.ac.in**

Note

Shortlisted candidates will be called for an interview. Selection will be based on qualifications, experience, and interview performance.