

# Indian Institute of Technology Indore

## Advertisement for Admission to Ph.D. Program in Electrical Engineering (EE) for Spring Semester of Academic Year (AY) 2026-27

**(WALK-IN INTERVIEW Under FA, FAP, and FAR Category only)**

IITI/Acad/PhD Admissions/Adv/EE/2026-27/03/01

August 13, 2026

IIT Indore invites applications from highly motivated and research-oriented students for admission to its PhD program in the Department of Electrical Engineering for the Spring Semester of Academic Year (AY) 2026-27 as per the below-mentioned categories of admission and time schedule. Candidates can visit the profiles of the faculty members listed below at the link <http://ee.iiti.ac.in/faculty.html> before applying. To know about different categories of admission (FA, FAP, and FAR), kindly refer to the main PhD advertisement webpage at <https://academic.iiti.ac.in/phdadvt.php>

### Time Schedule of PhD admission:

|                                                                                                                                        |                                                                       |
|----------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------|
| Last date of online application through the following link:<br><a href="https://phdapply.iiti.ac.in/">https://phdapply.iiti.ac.in/</a> | <b>September 06, 2026 (Sunday)</b><br><b>Latest by 24.00 hrs. IST</b> |
| Dates of PhD selection process ( <b>Written Test and Interview</b> )                                                                   | <b>September 10 and 11, 2026</b><br><b>(Thursday and Friday)</b>      |

### Minimum Educational Qualifications (MEQs) and Qualifying Examination

Educational Qualification: Master's degree in Electrical/ Electronics/ Electronics and Communication / Instrumentation and Control / Material Science/ Engineering Physics or any other relevant Department of Engineering / Technology with specialization in the areas of Communication and Signal Processing, RF and Microwave, Nanoelectronics and VLSI, Power electronics and Power Systems, or Control systems (with first division in the qualifying degree\*)

**AND**

Qualifying Examination: UGC/CSIR/DBT - JRF qualification **OR** DST INSPIRE fellowship **OR** Equivalent fellowship.

**OR**

Educational Qualification: Four-year Bachelor's degree OR five-year integrated degree in Electrical/ Electronics/ Electronics and Communication / Instrumentation and Control / Material Science/ Engineering Physics or any other relevant Department of Engineering / Technology (with first division in the qualifying degree\*)

**AND**

Qualifying Examination: UGC/CSIR/DBT - JRF qualification **OR** DST INSPIRE fellowship **OR** Equivalent fellowship.

**OR**

Educational Qualification: Master's degree in Mathematics / Physics or any other relevant Department of Science (with first division in the qualifying degree\*)

**AND**

Qualifying Examination: UGC/CSIR/DBT - JRF qualification OR DST INSPIRE fellowship OR Equivalent fellowship.

Candidates without having any Equivalent fellowship may be considered under FAP mode, if faculty members are having vacancies under sponsored projects. For more details, please refer to: <https://academic.iiti.ac.in/phdadvt.php>.

\*The definition of first division in qualifying degree as per IIT Indore's rules is as follows:

- A minimum of 60% (55%<sup>#</sup>) marks in aggregate, OR
- A First class as specified by the university, OR
- A minimum Cumulative Grade Point Average (CGPA) / Cumulative Performance Index (CPI) of 6.0 (5.5<sup>#</sup>) on a scale of 0 – 10, OR
- An equivalent to 6.0 (5.5<sup>#</sup>) CGPA/ CPI on other corresponding proportional requirements when the scales are other than 0 – 10.

<sup>#</sup>for SC/ST/PwD category

### Important Instructions:

1. All candidates, fulfilling the minimum eligibility criteria, must apply online through the website (<https://phdapply.iiti.ac.in/>).
2. After applying online, the signed application form along with the following documents should be sent by email to [admission-ee@iiti.ac.in](mailto:admission-ee@iiti.ac.in)
  - a) Self-attested photocopies/scanned originals of all relevant supporting documents such as **degree certificates, mark sheets from 10th class onwards, date of birth certificate, fellowship award letter, GATE score card, etc., that they wish to present before the selection committee.**
  - b) Receipt of fee (Rs.100/-) paid through SBI i-collect.
3. While submitting the application in the portal, the candidate must provide the **valid email addresses** of at least **two referees**, who have known the applicant in a professional capacity. An email will be sent to those referees requesting them to submit their recommendations within the specified deadline. **The recommendations are mandatory for PhD selection process.**

4. **DO NOT** send any form or documents by post.
5. Eligible candidates who have submitted online application (before the last date) and fulfill the minimum eligibility criteria are strongly encouraged to report at **10.00 AM** on **September 10, 2026 (Thursday)** at **Electrical Engineering Office, Room No. 1A-318B, Building Pod 1A, IIT Indore, Simrol, Indore, Madhya Pradesh – 453552**. **Candidates must take a note that no separate email/communication will be sent to applicants regarding shortlisted candidates and PhD selection process.**
6. PhD written test and interviews will commence at 10.30 AM on **September 10, 2026 (Thursday)**. The selection process may extend up to **September 11, 2026 (Friday)**. So, the candidates are therefore requested to make necessary travel and accommodation arrangements accordingly. **No communication in any form regarding accommodation, change of date, syllabus of written test and interview, request for conducting online interviews, etc. will be entertained.**
7. Eligible candidates should bring along the documents (original and photocopies) mentioned in instruction 2 on **September 10, 2026 (Thursday)** to present before the selection committee failing which they may not be considered for the selection process. The candidates must ensure that their referees submit the recommendations through online portal within the specified deadline.
8. No TA/DA will be paid for attending the PhD selection process. Candidates also must make their own accommodation arrangements.
9. Candidates who wish to appear for the PhD selection process and fulfill the eligibility criteria may also send their Resume/CV to the faculty member whose area is of interest to them. The areas of interest and detailed profiles of faculty members are given below. Candidates are encouraged to visit the webpage of faculty members listed below to know more about ongoing research work and areas of interest.
10. Mere fulfillment of the minimum eligibility criteria does not entitle anyone for admission into the PhD program in the Department of Electrical Engineering.



**Prof. Santosh Kumar Vishvakarma**

Prof. Santosh Kumar Vishvakarma is leading “Nanoscale Devices, VLSI Circuit and System Design” research group at IIT Indore. His research interests are VLSI Circuit and System Design including ASIC/SoC Design of Processor Design for Biomedical Application; Energy-Efficient and Reliable SRAM Memory Design; Enhancing Performance and Configurable Architecture for DNN Accelerators; SRAM/RRAM/MRAM based In-Memory Computing Architecture for Edge AI; Reliable, Secure Design for IoT Application and Design for Reliability; Silicon Photonics Circuits; and Quantum Logic and Circuits. He has a very strong collaboration in Industry and Academia across India and globe. As of now, 19 PhD scholars have graduated and 06 Indian patents have been granted from his research group. For details, please visit: <https://www.skvishvakarma.com/>. He may be contacted at his email id [skvishvakarma@iiti.ac.in](mailto:skvishvakarma@iiti.ac.in)



**Prof. Shaibal Mukherjee**

**Hybrid Nanodevice Research Group (HNRG)** led by Prof. Shaibal Mukherjee needs sincere and motivated PhD students to work in **RRAMs in Image Processing, Circuit Design; Quantum Sensors for Healthcare and Environment** (<https://hnrq.profiles.iiti.ac.in> and <https://www.quantech2m.com/>). HNRG has strong collaboration with industries and academia in India and in the USA, KAUST, Russia, France, Sweden, Italy, Japan, Australia, Taiwan, and Germany. **Candidates, having expertise in PCB design / CAD / FPGA / Python / Embedded Design are desirable.** Till date, 22 PhD students have graduated thesis from HNRG with 154+ journal papers, 110+ conference papers, 12 book/book chapters and 18 patents (granted: 14, filed and published: 4). Former PG and PhD graduates from HNRG are successfully placed in IIT, IMEC, Samsung, Synopsys, CDAC, NITs, IIITs, NVIDIA, POWERGRID (<https://hnrq.profiles.iiti.ac.in/history.php>). Interested candidates should send their resume at [shaibal@iiti.ac.in](mailto:shaibal@iiti.ac.in)



**Prof. Mukesh Kumar**

Prof. Mukesh Kumar is leading **Optoelectronic Nanodevice Research Laboratory (ONRL)**. His research interests include **Optoelectronic Devices, VLSI Technology, Microwave Photonics, Nanoelectronics, Integrated Photonics and Device Fabrication**. He has supervised 12 PhD-scholars so far. He is also serving as an adjunct-faculty at Purdue University, USA. He is looking for motivated and hard-working PhD-candidates with a background in **Electronics and related** areas. For further details, please visit <http://iiti.ac.in/people/~mukesh.kr>. Contact: [mukesh.kr@iiti.ac.in](mailto:mukesh.kr@iiti.ac.in)



**Dr. Pratibha Verma**

Dr. Pratibha Verma is an Assistant Professor in the Department of Electrical Engineering. Her research focuses on AI-assisted integrated circuit design, analog and mixed-signal design automation, and energy-harvesting power-management circuits. Her work includes LLM-based automation of analog circuit design, low-power interface circuits, and integrated power-management systems for piezoelectric and triboelectric energy harvesters. Interested candidates are encouraged to contact her at [pratibhaverma@iiti.ac.in](mailto:pratibhaverma@iiti.ac.in).



Prof. Ram Bilas Pachori

Prof. Ram Bilas Pachori works in the areas of Signal and Image Processing, Biomedical Signal Processing, Non-stationary Signal Processing, Speech Signal Processing, Brain-Computer Interface, Machine Learning, AI and IoT in Healthcare. He has 413 publications which include journal papers (264), conference papers (107), books (12), and book chapters (30). His publications have more than 22,200 citations with an h-index of 82 as per Google Scholar. He has supervised 28 Ph.D. students for their theses. He is looking for the Ph.D. students to work in the above mentioned research areas. Please visit his homepage for more details: <http://iiti.ac.in/people/~pachori/>



Prof. Vimal Bhatia

Prof. Vimal Bhatia (<http://iiti.ac.in/people/~vbhatia/> / [vbhatia@iiti.ac.in](mailto:vbhatia@iiti.ac.in)) is leading collaborations with researchers from the **UK, Ireland, Norway, Finland, France, Canada, Czech Republic**, and the **US**, with more than 300 peer-reviewed publications, 5 patent granted and 18 PhD thesis has been submitted. Research on a) Performance analysis of beyond 5G/6G communications, b) OFDM, MIMO, NOMA, Cognitive Radio, Visible Light Communications, Quantum Communications c) Bio-inspired image processing, biometry, radar using machine and deep learning algorithms. Bright and highly motivated candidates, having background in **Communications/Signal Processing/Mathematics/Statistics/Electronics/Electrical Engineering/Computer Science or equivalent** are encouraged to apply. Former PG students placed in IIT, NIT, IIIT, NMIMS, Australia, Canada, Saudi Arabia, UK, EU, and Qualcomm.



Dr. Swaminathan R

Dr. Swaminathan's ([swamiramabadran@iiti.ac.in](mailto:swamiramabadran@iiti.ac.in)) **Future Generation Communication Systems research group** works on 6G Wireless Systems, which include Non-Terrestrial Networks (NTNs), Intelligent Reflecting Surfaces (IRS) Aided FSO/RF/THz Communications, etc., and Receiver Design using Deep Learning Techniques. Dr. Swaminathan is the author or co-author of more than 100 IEEE Journal and Conference publications. Interested candidates who have a background in Wireless Communications, Signal Processing for Communications, Coding Theory, etc. as well as good in programming are encouraged to apply. Please visit <https://swamiramabadran.wixsite.com/website>



Dr. Ayush Tripathi

Dr. Ayush Tripathi is an Assistant Professor in the Department of Electrical Engineering at IIT Indore, where he is establishing the COMputational Signal INformatics and Exploration (COSINE) Lab, focusing on biomedical signal processing and AI applications in healthcare. His research spans brain-computer and human-machine interfaces, EEG analysis, automated sleep analysis, and pathological speech processing. More details can be found here <https://ayushayt.github.io/COSINE> and interested candidates are encouraged to contact me at [ayush.tripathi@iiti.ac.in](mailto:ayush.tripathi@iiti.ac.in)



Dr. Himali Singh

Dr. Himali Singh ([himali\\_singh@iiti.ac.in](mailto:himali_singh@iiti.ac.in)) is an Assistant Professor in the Department of Electrical Engineering at IIT Indore. Her research interests include estimation theory, radar signal processing, target tracking, and intelligent counter-adversarial systems. She is seeking highly motivated candidates with a strong foundation in signal processing and probability to join her research group.



Dr. Subhadeep Paladhi

Dr. Subhadeep Paladhi is leading a research group in the Department of Electrical Engineering at IIT Indore in the area of **Power System Protection**, with a focus on the growing large-scale integration of renewable energy sources in power systems. The ongoing research in his group includes the cutting-edge technologies in the following domains: 'Phasor-based relaying in presence of converter-Interfaced sources', 'Time-domain high speed line protection', 'AI/ ML-based grid monitoring' and 'Resilient system operation using digital twin'. Highly motivated candidates, having a strong background in power systems, are encouraged to apply. For more details, visit: <https://sites.google.com/view/subhadeep-paladhi>. For any prior interaction, the candidates may contact him through [spaladhi@iiti.ac.in](mailto:spaladhi@iiti.ac.in).

**For any queries, please contact:**

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